

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	206	(convert\$5 create\$5 generat\$5) SAME (circuit NEAR5 design) SAME (circuit NEAR5 layout) SAME file	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/11/22 17:49
L4	47	L1 AND HDL	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/11/22 17:38
L6	8	circuit SAME layout SAME file SAME ALLEGRO	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/11/22 17:25
L7	6	(convert\$5 create\$5 generat\$5) SAME circuit SAME layout SAME file SAME ALLEGRO	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/11/22 17:27
L8	25	automatic SAME (convert\$5 create\$5 generat\$5) SAME (circuit NEAR5 design) SAME (circuit NEAR5 layout) SAME file	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/11/22 17:50

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converting circuit design file to circuit layout file

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